

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
)  
Katsuhiko HIEDA )  
) Group Art Unit:  
Serial No.: Not Yet Assigned )  
) Examiner:  
Filed: August 29, 2003 )  
)  
For: SEMICONDUCTOR DEVICE )  
AND METHOD OF )  
MANUFACTURING THE SAME )

**MAIL STOP PATENT APPLICATION**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicant respectfully requests that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is listed on the accompanying PTO-1449 and is in a non-English language:

1. Japan Patent Application (KOKAI) Publication No. 2001-189434 discloses a storage node electrode layout which is at an angle with Bit Line. (Fig. 21). The

relevance of this document is also discussed at page 2 of the specification of the present application.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and applicant determines that the cited document does not constitute "prior art" under United States law, applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of the document. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

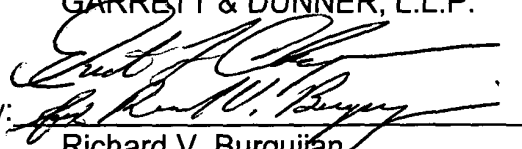
If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: August 29, 2003

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## INFORMATION DISCLOSURE CITATION

|                  |                 |            |  |
|------------------|-----------------|------------|--|
| Atty. Docket No. | 04329.3128      | Serial No. |  |
| Applicant        | Katsuhiko HIEDA |            |  |
| Filing Date      | August 29, 2003 | Group:     |  |

| U.S. PATENT DOCUMENTS |  |                 |            |      |       |           |                            |
|-----------------------|--|-----------------|------------|------|-------|-----------|----------------------------|
| Examiner Initial*     |  | Document Number | Issue Date | Name | Class | Sub Class | Filing Date If Appropriate |
|                       |  |                 |            |      |       |           |                            |
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| FOREIGN PATENT DOCUMENTS |  |                 |                  |         |       |           |                       |
|--------------------------|--|-----------------|------------------|---------|-------|-----------|-----------------------|
|                          |  | Document Number | Publication Date | Country | Class | Sub Class | Translation Yes or No |
|                          |  | 2001-189434     | 07/10/2001       | Japan   |       |           | No                    |
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| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) |  |
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|---|---|
| Examiner  | Date Considered   |
| *Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |   |
| Form PTO 1449   | Patent and Trademark Office - U.S. Department of Commerce |